

Notice of References Cited

Application/Control No.

10/816,791

Applicant(s)/Patent Under
Reexamination
MACCHETTI ET AL.

Examiner

MARTIN JERIKO P. SAN JUAN

Art Unit

2132

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-4,275,265	06-1981	Davida et al.	380/29
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Logic and Computer Design Fundamentals. Chapter 4 -- Combinational Functions and Circuits." 2004. Pearson Education. Slides 9-11. http://faculty.ksu.edu.sa/drfrass/discrete%20tutorials/LCDF3_Chap_04.ppt
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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